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NOTICE OF ALLOWANCE AND FEE(S) DUE

000466

7590

05/17/2004

YOUNG & THOMPSON 745 SOUTH 23RD STREET 2ND FLOOR ARLINGTON, VA 22202 EXAMINER

BRITT, CYNTHIA H

ART UNIT PAPER NUMBER

2133

DATE MAILED: 05/17/2004

APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 09/831,525 05/10/2001 Romain Desplats BE-9197 5660

TITLE OF INVENTION: METHOD AND INSTALLATION FOR FAST FAULT LOCALIZATION IN AN INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	08/17/2004

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. <u>PROSECUTION ON THE MERITS IS CLOSED</u>. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE REFLECTS A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE APPLIED IN THIS APPLICATION. THE PTOL-85B (OR AN EQUIVALENT) MUST BE RETURNED WITHIN THIS PERIOD EVEN IF NO FEE IS DUE OR THE APPLICATION WILL BE REGARDED AS ABANDONED.

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Applicant claims SMALL ENTITY status.
 See 37 CFR 1.27.

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IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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000466 7590 05/17/2004 YOUNG & THOMPSON 745 SOUTH 23RD STREET 2ND FLOOR ARLINGTON, VA 22202					have its own certifica	te of mailing or transmission.	0. 101	
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							(Depositor's name)	
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APPLICATION NO.	FIL	ING DATE	FII	RST NAMED INVEN	TOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/831,525	05	/10/2001	Romain Desplats		 	BE-9197	5660	
TITLE OF INVENTION:	METHOD A	ND INSTALLATI	ON FOR FAST FA	AULT LOCALIZAT	TION IN AN INTEGE	RATED CIRCUIT		
APPLN. TYPE	SMA	LL ENTITY	ISSUE FEE	Pt	IBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
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YOUNG & THO	OMPSON O STREET 2ND FLOO	R	BRITT, CYNTHIA H	
ARLINGTON, V.		-	ART UNIT PAPER NUMBE 2133	
			DATE MAILED: 05/17/200-	4

Determination of Patent Term Extension under 35 U.S.C. 154 (b)

(application filed after June 7, 1995 but prior to May 29, 2000)

The Patent Term Extension is 0 day(s). Any patent to issue from the above-identified application will include an indication of the 0 day extension on the front page.

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Extension is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) system (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (703) 305-1383. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at (703) 305-8283.

	Application No. Applicant(s)			
	09/831,525	DESPLATS ET AL.	DESPLATS ET AL.	
Notice of Allowability	Examiner	Art Unit		
	Cynthia Britt	2133		
The MAILING DATE of this communication app. All claims being allowable, PROSECUTION ON THE MERITS I herewith (or previously mailed), a Notice of Allowance (PTOL-8 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3	S (OR REMAINS) CLOSED in 5) or other appropriate commun RIGHTS. This application is su	this application. If not include nication will be mailed in due	ed course. THIS	
1. This communication is responsive to 4/29/04.				
2. 🔀 The allowed claim(s) is/are <u>1-35</u> .				
3. The drawings filed on are accepted by the Examir	ner.			
4. ☑ Acknowledgment is made of a claim for foreign priority a) ☑ All b) ☐ Some* c) ☐ None of the: 1. ☑ Certified copies of the priority documents ha 2. ☐ Certified copies of the priority documents ha 3. ☐ Copies of the certified copies of the priority of International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. ☐ A SUBSTITUTE OATH OR DECLARATION must be sub INFORMAL PATENT APPLICATION (PTO-152) which give including changes required by the Notice of Draftsperior (a) ☑ including changes required by the Notice of Draftsperior (b) ☐ including changes required by the attached Examine Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in	eve been received. Eve been received in Application documents have been received. Every of this communication to file and the summer of this application. Every of this application of the summer o	in this national stage applicate reply complying with the red MINER'S AMENDMENT or Noteclaration is deficient. (PTO-948) attached in the Office action of the drawings in the front (not the statistic).	quirements OTICE OF	
 DEPOSIT OF and/or INFORMATION about the department attached Examiner's comment regarding REQUIREMEN 			Note the	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SE Paper No./Mail Date	8) 6. ⊠ Interview Su Paper No./N B/08), 7. ⊠ Examiner's A	Mail Date <u>11</u> . Amendment/Comment	,	
 Examiner's Comment Regarding Requirement for Deposit of Riological Material 	t 8. ⊠ Examiner's 9. ☐ Other	Statement of Reasons for Allo	wance	
of Biological Material	9. 🔲 Other	•		

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EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Roland Long #41,949 on May 10th 2004.

The application has been amended as follows: Claim 31, line 11 (on page 16 of amendment) "...black on completion of the adaptive threholding;" should be "...black on completion of the adaptive thresholding;"

Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

The present invention pertains to a system and method for locating a fault in an integrated circuit. The integrated circuit is connected with a tester, which applies test vectors to the circuit under test, and measures the current consumption at rest IDDQ, to detect short circuit faults. Furthermore, the integrated circuit is placed in potential contrast: electron microscopy imaging system, detecting secondary electrons that produce a set of vector images representing equipotential lines created by the circuit tracks and logic gates. A sequence of location vectors is generated on the basis of the previously formed sequence of test vectors. Each test vector for which the binary signal of at least one input terminal is capable of changing logical state is broken down into a

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plurality of location vectors, the binary signals of which do not change logical state. The location vectors are applied to the input terminals step by step. The value IDDQ is measured for each vector, and it is determined whether that value is normal. The location vectors for which the value IDDQ is abnormal are applied next, followed by a set of images of the circuit potential in which contrast is produced. Finally, by comparing the abnormal vector images with prerecorded reference images, the malfunctioning logic gate is located in the integrated circuit.

The prior arts of record (U.S. Patent No. 5,790,565 Sakaguchi being the closest example of prior art) disclose a test system for an integrated circuit, which uses a combination of a functional test with a current consumption test at rest IDDQ. It is clear that this test configuration is different from the one used in the present invention.

However, none of the prior art of record, taken either alone or in combination discloses the IDDQ test in combination with potential contrast electron microscopy imaging system. As such, modification of the prior arts of record can only be motivated by hindsight reasoning, or by changing the intended use and function of the prior arts themselves. As such, it is not clear that one of ordinary skill in the art at the time of the invention would have made the necessary modifications to the prior art of record to encompass the limitations set forth in the independent claims 1 and 33.

Drawings

The drawings were received on April 29, 2004. These drawings are not acceptable for publication.

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The drawings filed on April 29, 2004 are acceptable subject to correction of the informalities indicated in the interview of May 10, 2004. Some of the corrected drawings submitted with the requested corrections have text over the lines of the drawings. This overwrite render the drawings unclear to the reader. In order to avoid abandonment of this application, correction is required in reply to the Office action. The correction will not be held in abeyance.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cynthia Britt whose telephone number is 703-308-2391. The examiner can normally be reached on Monday - Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 703-305-9595. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Cynthia Britt Examiner Art Unit 2133

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